

Figure 1

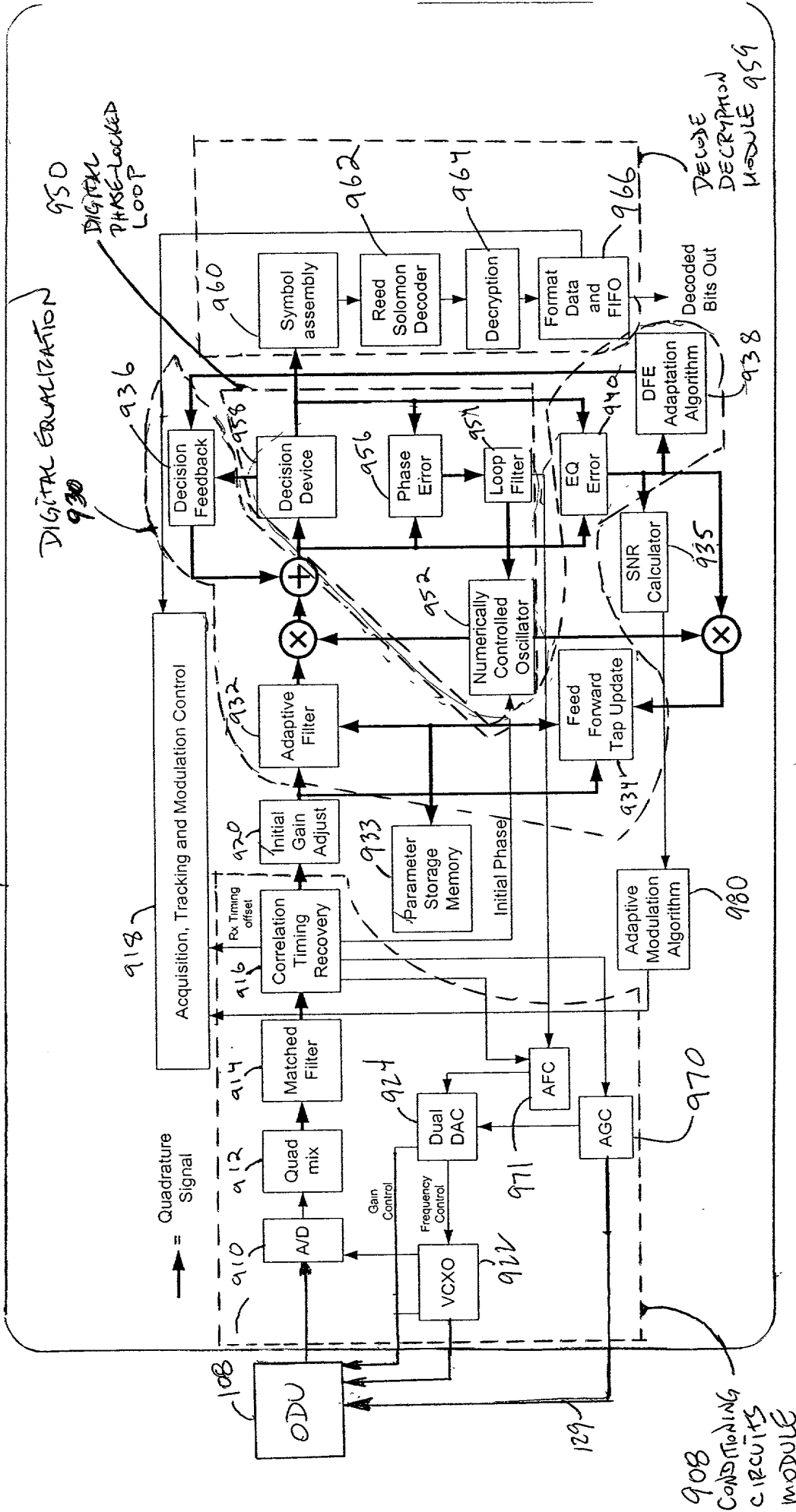


Figure 2

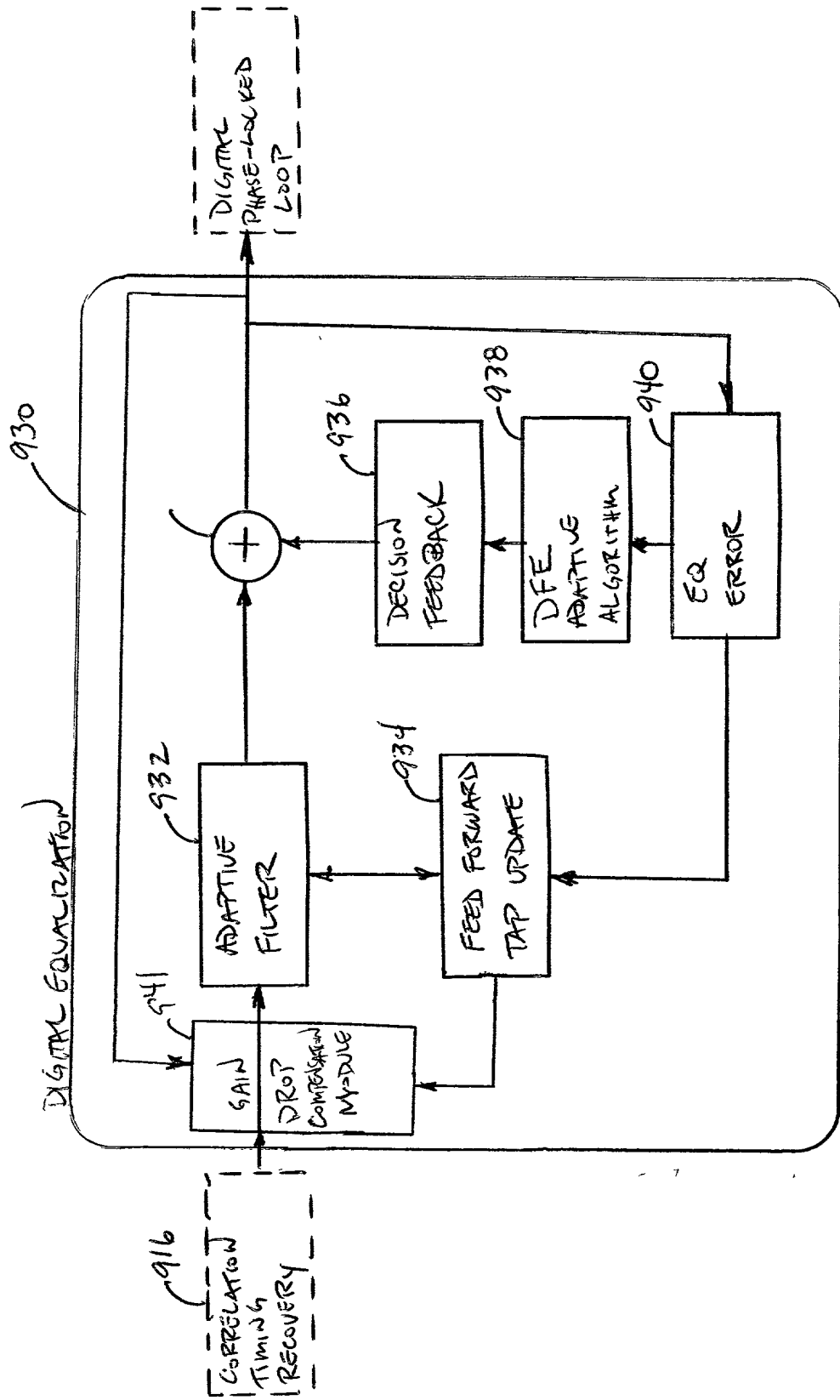


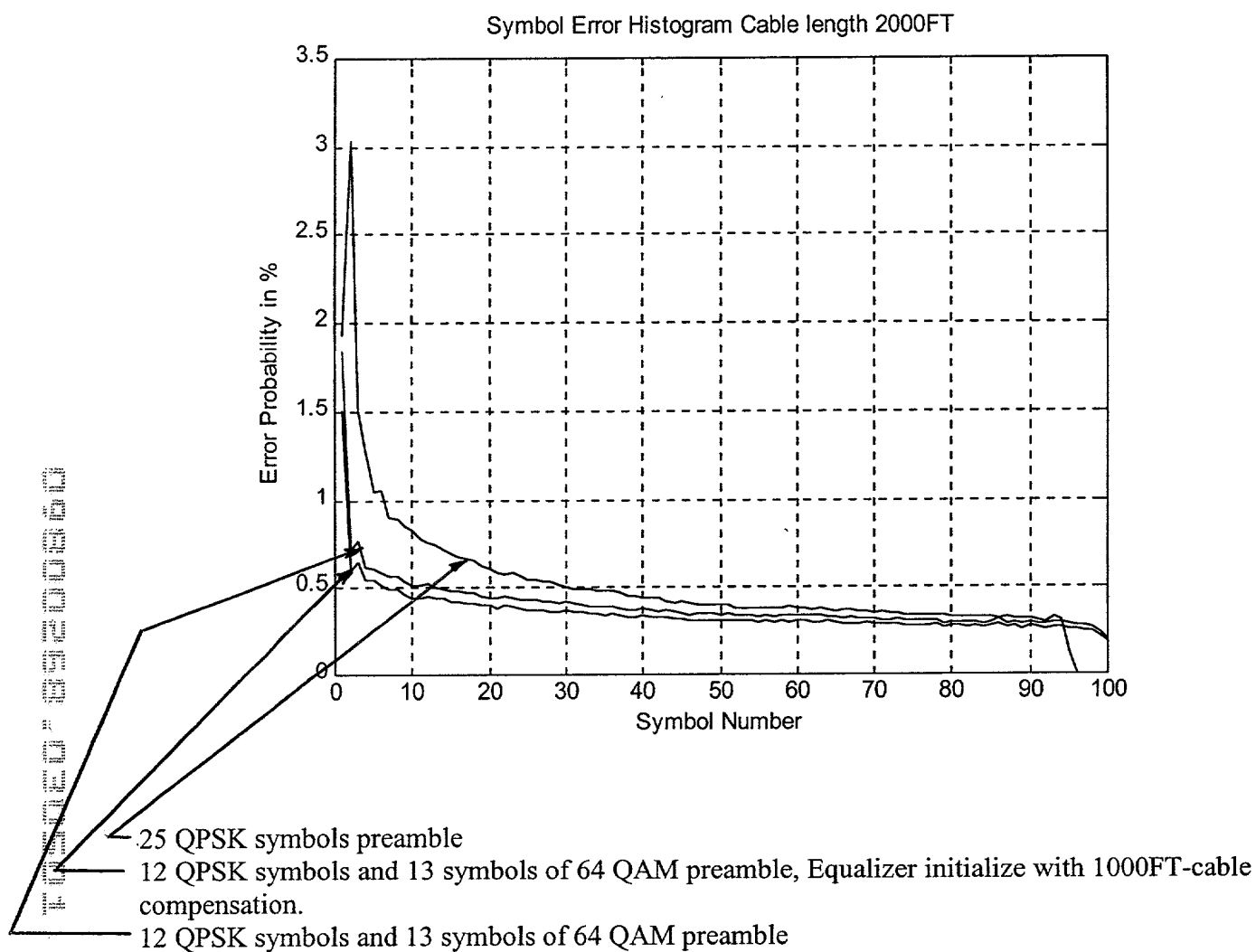
FIG. 3

This Multiplier has a 3-40MHz clock

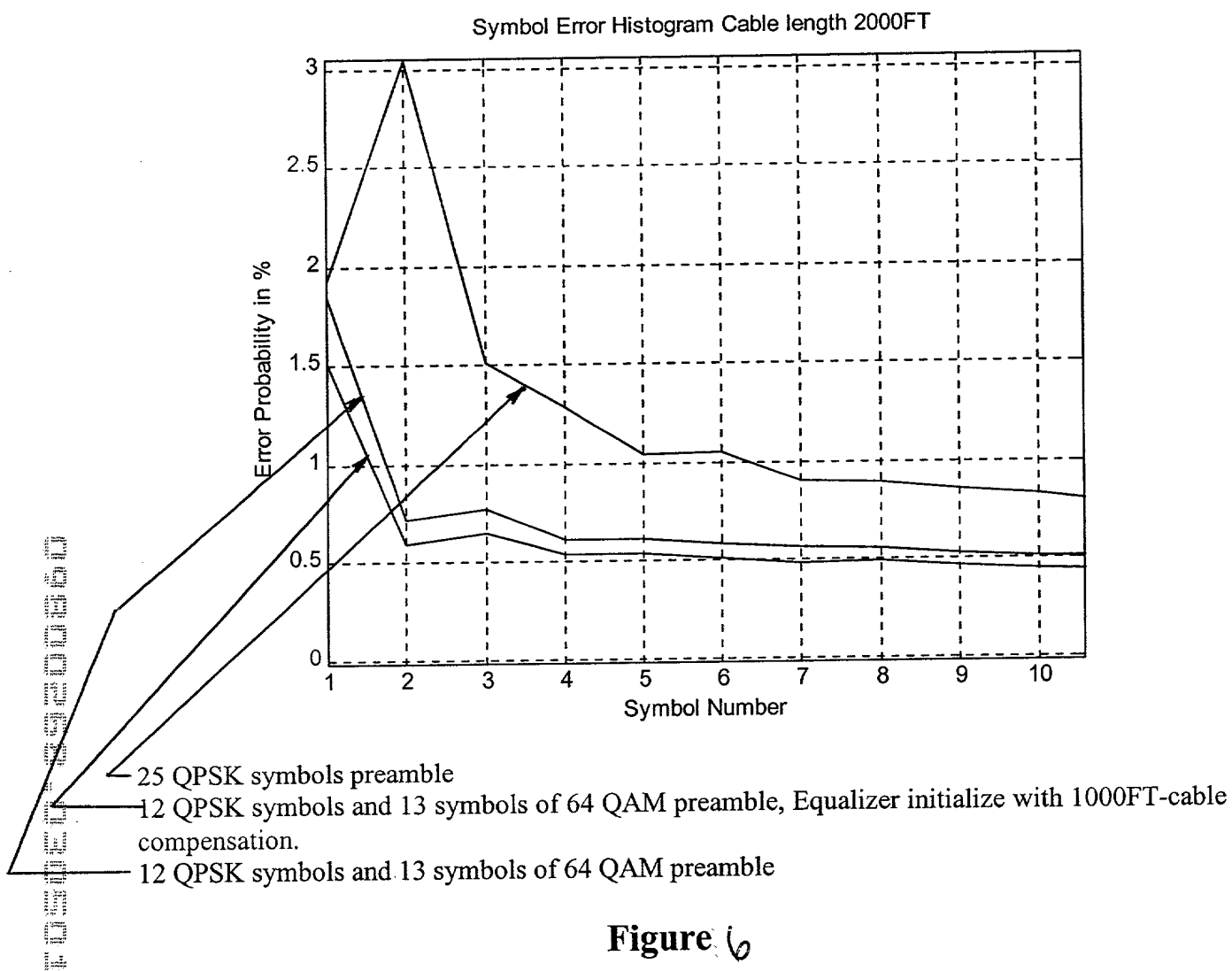
1930



FIGURE 4



**Figure 5**



**Figure 6**

Cable Length	BER with Preamble Type 1	BER with Preamble Type 2	RS Error Rate with Preamble Type 1	RS Error Rate with Preamble Type 2
1000FT	5.42e-4	4.22e-4	6e-6	No Error over a million bursts.
2000FT	8.88e-4	6.55e-4	1.2e-4	1.6e-5

**Figure 7**

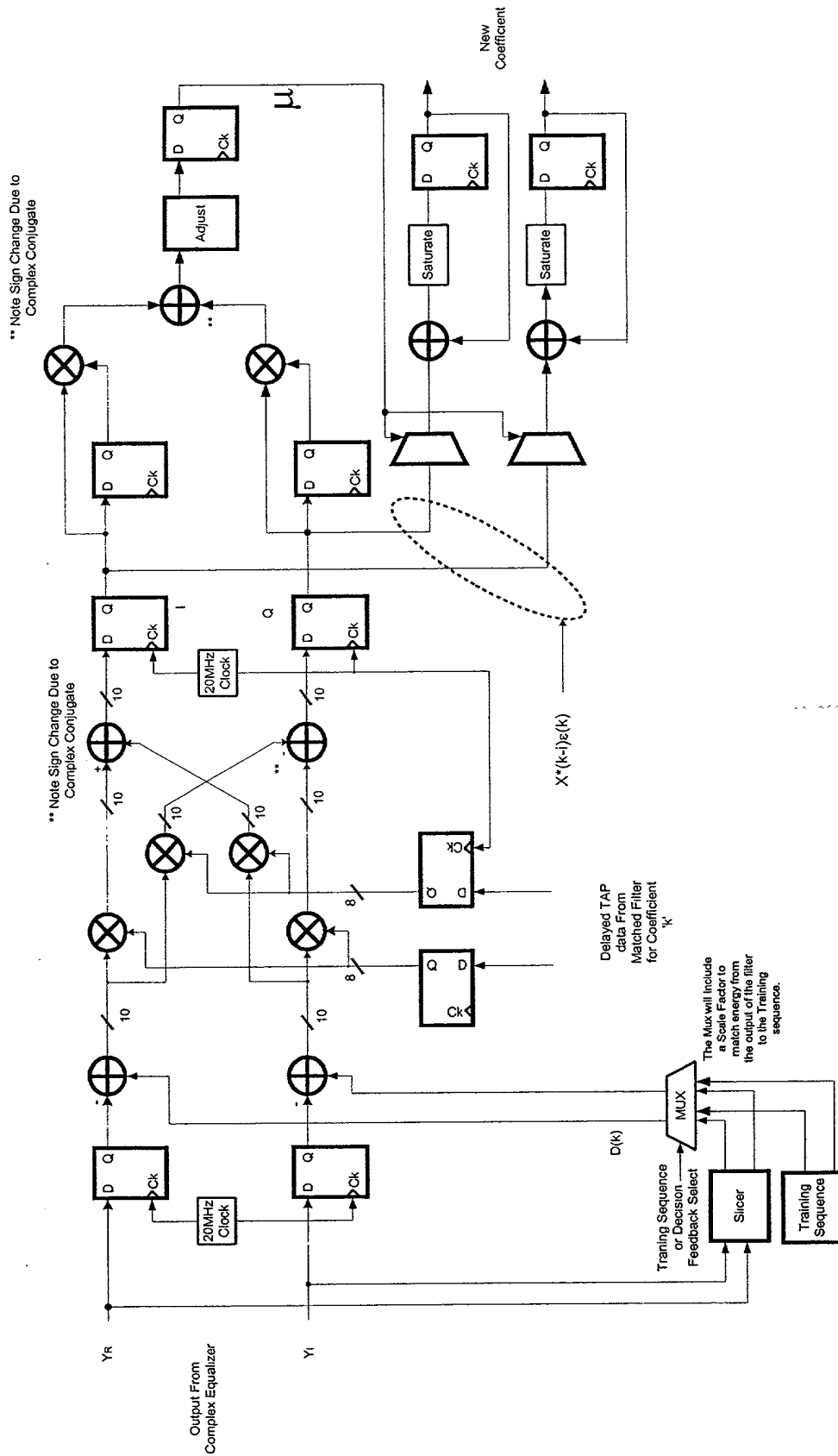
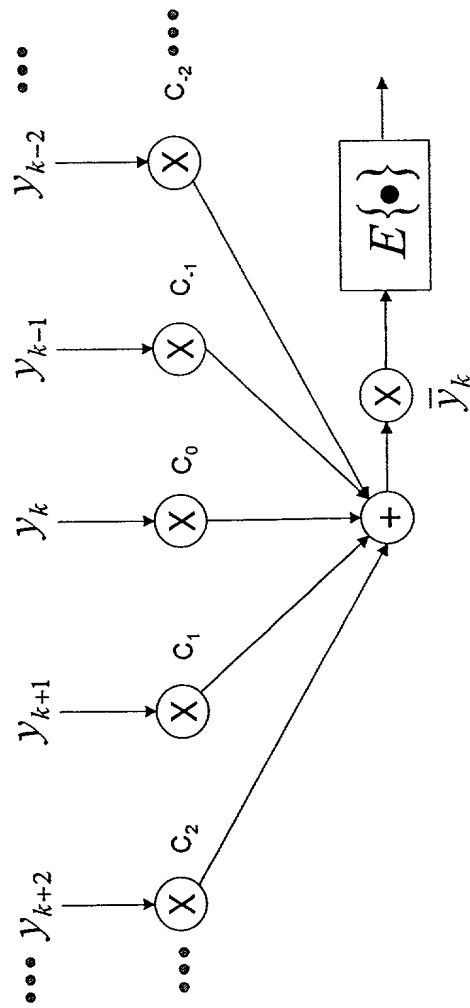
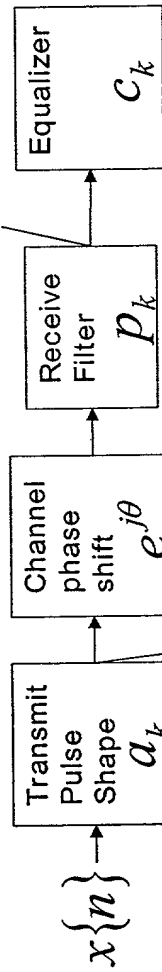


Figure 8



$$y_k = e^{j\theta} \sum_{j=-2N}^{2N} x_{k-j} h_j,$$

$$h_k = a_k * p_k$$



$$s_k = \sum_{j=-N}^N x_{k-j} a_j$$

Figure 9



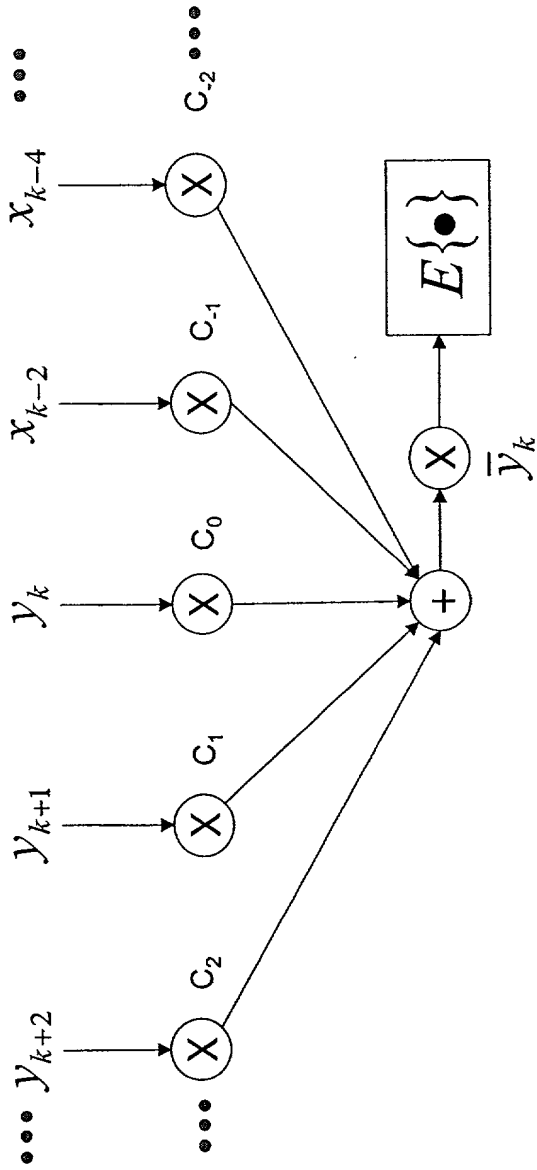


Figure 10

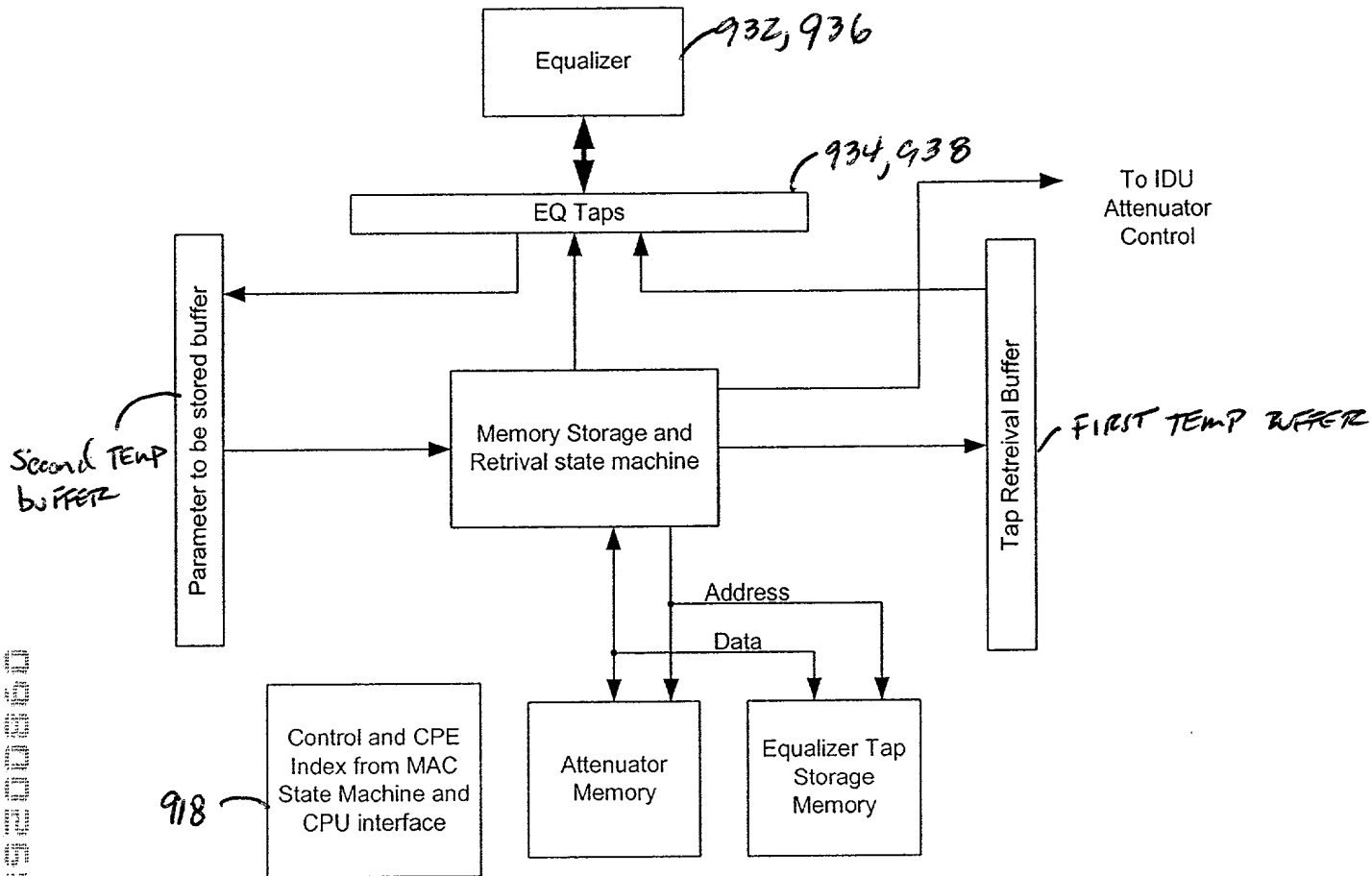


FIGURE 11

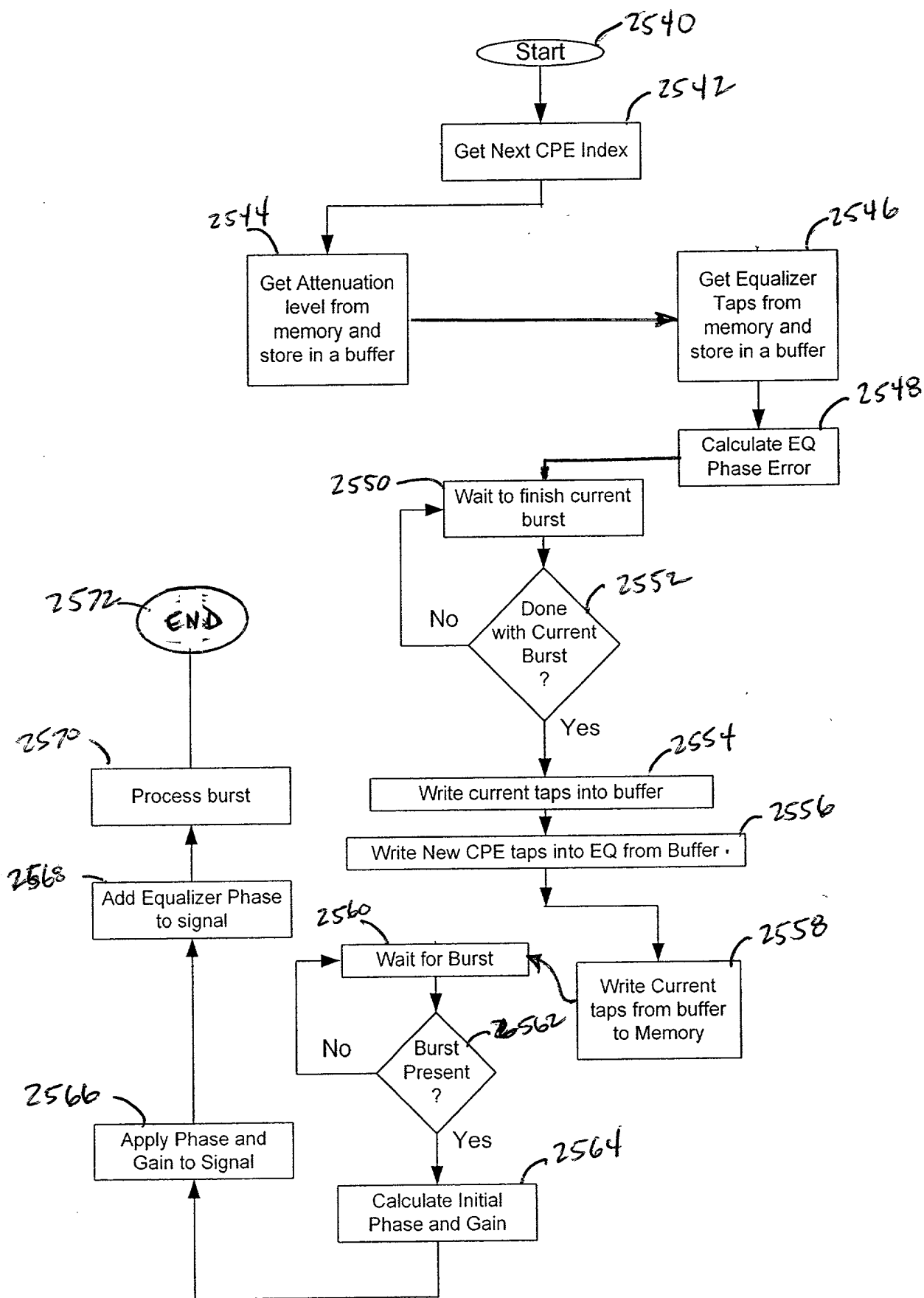


FIGURE 12

# DIGITAL EQUALIZATION PARAMETER STORAGE AND RECOVERY

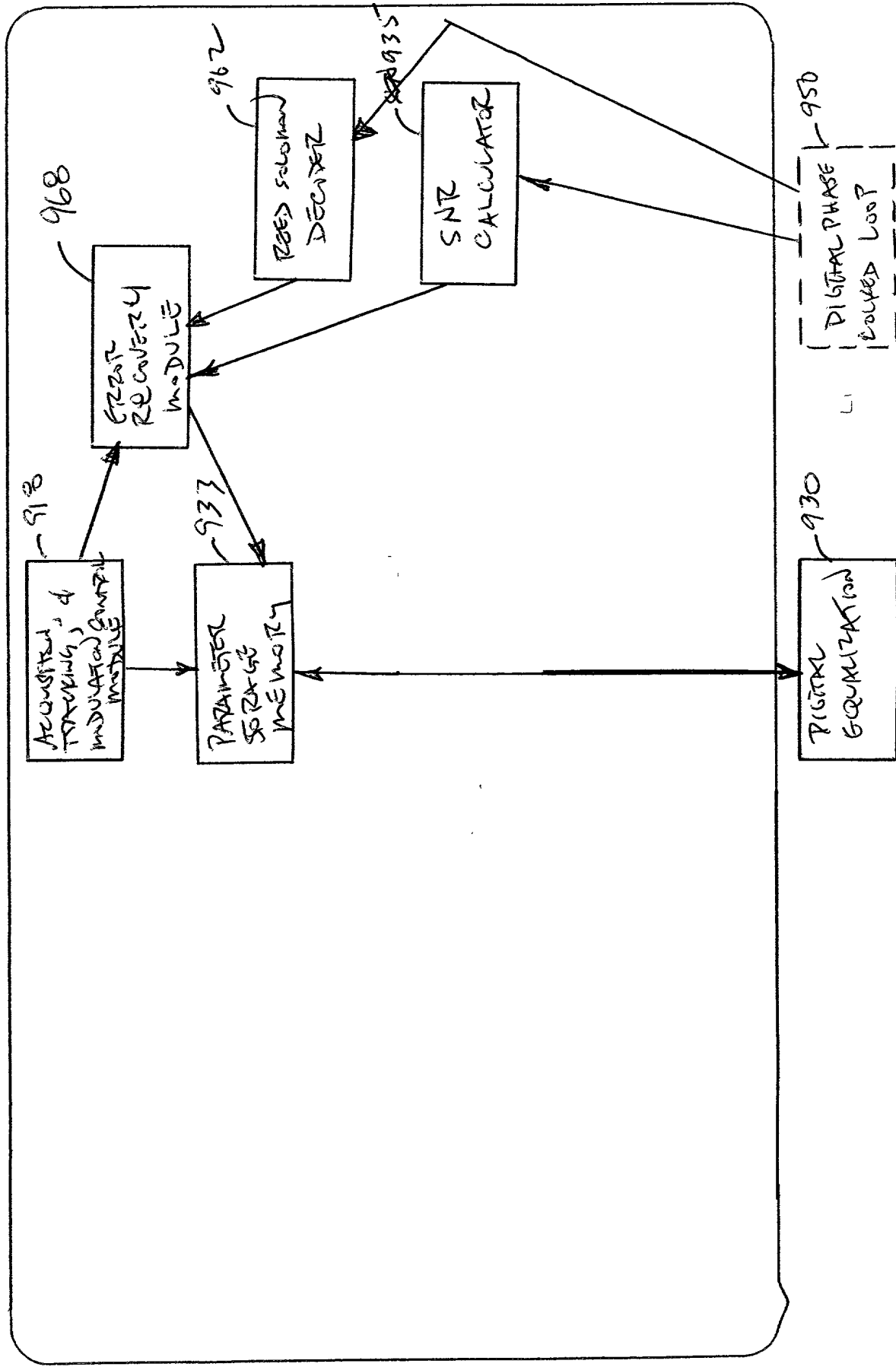


FIG. 13

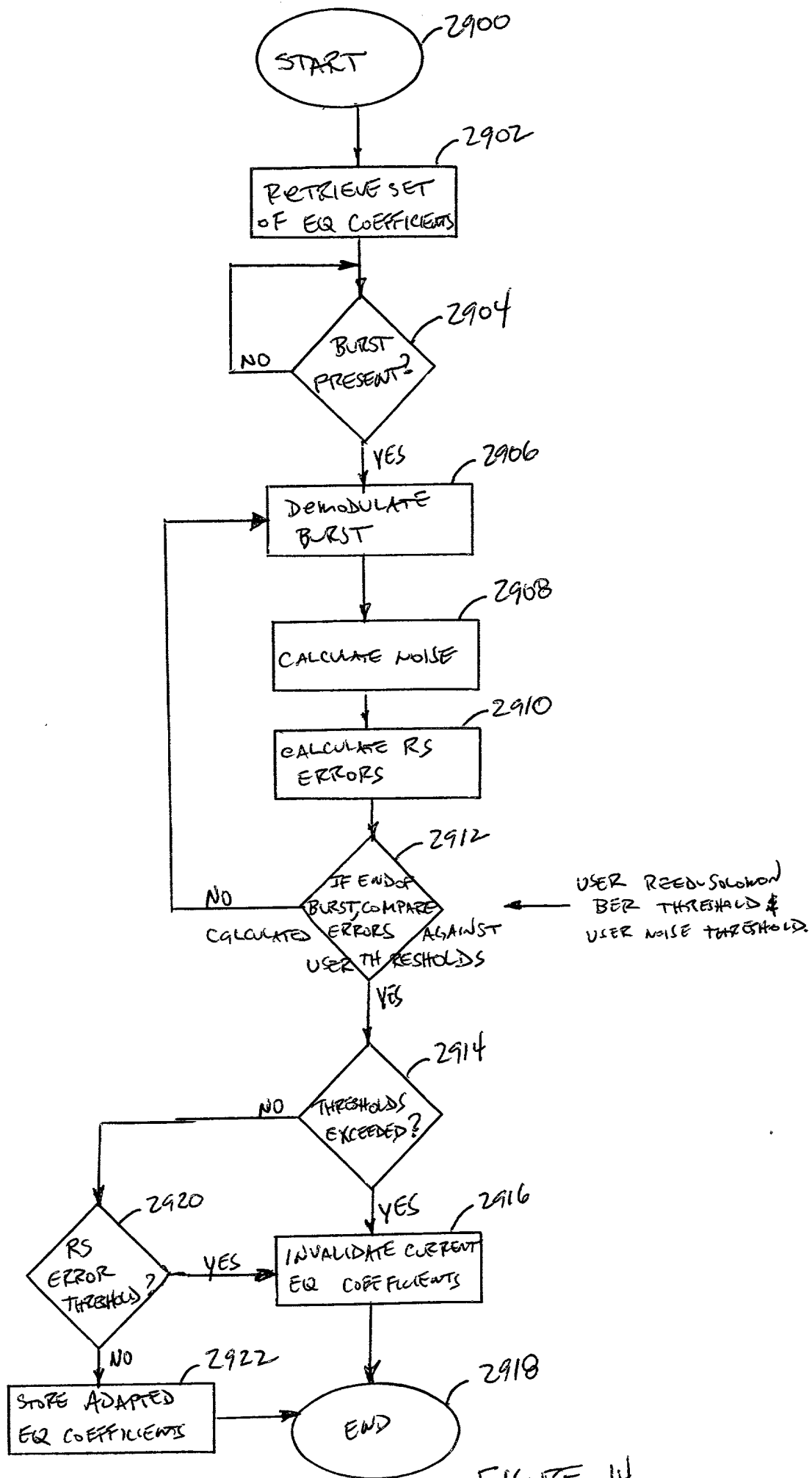


FIGURE 14